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		6684289.pn. or 20040123020.pn. or 20040111555.pn. or 20040064636.pn.		
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<u>L2</u>	365/185.11.ccls.	918	<u>L2</u>
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1 Simultaneous code execution and data storage in a single flash memory chip for real time wireless communication systems

Brown, C.; Hasbun, R.;

Circuits and Systems, 1997. Proceedings of the 40th Midwest Symposium on , Volume: 2 , 3-6 Aug. 1997

Pages:740 - 745 vol.2

[Abstract] [PDF Full-Text (672 KB)] IEEE CNF

2 A 1.8 V 128 Mb 125 MHz multi-level cell flash memory with flexible read while write

Elmhurst, D.; Bains, R.; Bressie, T.; Bueb, C.; Carrieri, E.; Chauhan, B.; Chrisman, N.; Dayley, M.; De Luna, R.; Fan, K.; Goldman, M.; Govindu, P.; Huq, A.; Khandaker, M.; Kreifels, J.; Krishnamachari, S.; Lavapie, P.; Loe, K.; Ly, T.; Marvin, F.; Melcher, R.; Monasa, S.; Nguyen, Q.; Pathak, B.; Proescholdt, A.; Rahman, T.; Srinivasan, B.; Sundaram, R.; Walimbe, P.; Ward, D.; Zeng, D.R.; Zhang, H.;

Solid-State Circuits Conference, 2003. Digest of Technical Papers. ISSCC. 2003 IEEE International , 2003

Pages:286 - 287 vol.1

[Abstract] [PDF Full-Text (378 KB)] [Extended Object] IEEE CNF

3 A 1.8 V 64 Mb 100 MHz flexible read while write flash memory [in CMOS]

Pathak, B.; Cabrera, A.; Christensen, G.; Darwish, A.; Goldman, M.; Haque, R.; Jorgensen, J.; Kajley, R.; Ly, T.; Marvin, F.; Monasa, S.; Nguyen, Q.; Pierce, D.; Sendrowski, A.; Sharif, I.; Shimoyoshi, H.; Smidt, A.; Sundaram, R.; Taub, M.; Tran, W.; Trivedi, R.; Walimbe, P.; Yu, E.;

Solid-State Circuits Conference, 2001. Digest of Technical Papers. ISSCC. 2001 IEEE International , 5-7 Feb. 2001

Pages:32 - 33, 424

[Abstract] [PDF Full-Text (220 KB)] IEEE CNF

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(partition)<sentence>(ram<or>volatile<or>sram<or>

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Klein, K.; Koetzle, G.; Miersch, E.F.; Schettler, H.; Schulz, U.; Wagner, O.;
Solid-State Circuits, IEEE Journal of, Volume: 22, Issue: 5, Oct 1987
Pages:783 - 789

[\[Abstract\]](#) [\[PDF Full-Text \(1464 KB\)\]](#) **IEEE JNL****2 Co-RAM: combinational logic synthesis applied to software partitions for mapping to a novel memory device**

Weber, S.J.; Paul, J.M.; Thomas, D.E.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on, Volume:
9, Issue: 6, Dec. 2001
Pages:805 - 812

[\[Abstract\]](#) [\[PDF Full-Text \(107 KB\)\]](#) **IEEE JNL****3 Exploiting symmetry when verifying transistor-level circuits by symbolic trajectory evaluation: a summary**

Pandey, M.; Bryant, R.E.;
Circuits and Systems Magazine, IEEE, Volume: 1, Issue: 4, 2001
Pages:38 - 42

[\[Abstract\]](#) [\[PDF Full-Text \(171 KB\)\]](#) **IEEE JNL****4 Reconfigurable caches and their application to media processing**

Ranganathan, P.; Adve, S.; Jouppi, N.P.;
Computer Architecture, 2000. Proceedings of the 27th International Symposium
on, 10-14 June 2000
Pages:214 - 224

[\[Abstract\]](#) [\[PDF Full-Text \(1148 KB\)\]](#) **IEEE CNF****5 Memory design for vectorization**

Sharma, N.K.; Iconomidou, A.;

Electronics, Circuits, and Systems, 1996. ICECS '96., Proceedings of the Third IEEE International Conference on , Volume: 2 , 13-16 Oct. 1996
Pages:812 - 815 vol.2

[Abstract] [PDF Full-Text (340 KB)] IEEE CNF

6 A manpack portable LPC 10 vocoder

Fette, B.; Rimpö, C.; Kish, J.;
Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '84. , Volume: 9 , Mar 1984
Pages:786 - 789

[Abstract] [PDF Full-Text (144 KB)] IEEE CNF

7 Mode partition noise in vertical cavity surface emitting lasers

Lee, H.L.T.; Ram, R.J.;
Advanced Semiconductor Lasers and Applications/Ultraviolet and Blue Lasers and Their Applications/Ultralong Haul DWDM Transmission and Networking/WDM Components, 2001 Digest of the LEOS Summer Topical Meetings , 30 July-1 Aug. 2001
Pages:2 pp.

[Abstract] [PDF Full-Text (160 KB)] IEEE CNF

8 Soluble dodecylsulfanylphthalocyanines as sensitive coatings for chemical sensors in gas phase

Altindal, A.; Patel, R.; Zhou, R.; Josse, F.; Ozturk, Z.Z.; Bekaroglu, O.;
Frequency Control Symposium, 1998. Proceedings of the 1998 IEEE International , 27-29 May 1998
Pages:676 - 684

[Abstract] [PDF Full-Text (800 KB)] IEEE CNF

9 A highly available replication control protocol using volatile witnesses

Paris, J.-F.;
Distributed Computing Systems, 1994., Proceedings of the 14th International Conference on , 21-24 June 1994
Pages:536 - 543

[Abstract] [PDF Full-Text (592 KB)] IEEE CNF

10 Hardware realisation of artificial neural network with application to information coding

Young, D.; Lee, A.W.H.; Cheng, L.M.;
Neural Networks, 1994. IEEE World Congress on Computational Intelligence., 1994 IEEE International Conference on , Volume: 6 , 27 June-2 July 1994
Pages:3982 - 3985 vol.6

[Abstract] [PDF Full-Text (240 KB)] IEEE CNF

11 Using volatile witnesses to extend the applicability of available copy protocols

Paris, J.F.;
Management of Replicated Data, 1992., Second Workshop on the , 12-13 Nov. 1992
Pages:30 - 33

[Abstract] [PDF Full-Text (252 KB)] IEEE CNF

12 The GPA machine: a generally partitionable MSIMD architecture

Bridges, T.;

Frontiers of Massively Parallel Computation, 1990. Proceedings., 3rd Symposium
on the , 8-10 Oct. 1990

Pages:196 - 203

[Abstract] [PDF Full-Text (492 KB)] IEEE CNF

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(correction<or>detection)<paragraph>memory<para>

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1 Chalcogenide memory arrays: characterization and radiation effects

Maimon, J.D.; Hunt, K.K.; Burcin, L.; Rodgers, J.;
Nuclear Science, IEEE Transactions on , Volume: 50 , Issue: 6 , Dec. 2003
Pages:1878 - 1884

[Abstract] [PDF Full-Text (868 KB)] IEEE JNL

2 A survey of progress in coding theory in the Soviet Union

Kautz, W.; Levitt, K.;
Information Theory, IEEE Transactions on , Volume: 15 , Issue: 1 , Jan 1969
Pages:197 - 244

[Abstract] [PDF Full-Text (7336 KB)] IEEE JNL

3 An overview of logic architectures inside flash memory devices

Silvagni, A.; Fusillo, G.; Ravasio, R.; Picca, M.; Zanardi, S.;
Proceedings of the IEEE , Volume: 91 , Issue: 4 , April 2003
Pages:569 - 580

[Abstract] [PDF Full-Text (654 KB)] [Full-Text HTML] IEEE JNL

4 SRAM based re-programmable FPGA for space applications

Wang, J.J.; Katz, R.B.; Sun, J.S.; Cronquist, B.E.; McCollum, J.L.; Speers, T.M.; Plants, W.C.;
Nuclear Science, IEEE Transactions on , Volume: 46 , Issue: 6 , Dec. 1999
Pages:1728 - 1735

[Abstract] [PDF Full-Text (448 KB)] IEEE JNL

5 Design of a digital gauss meter for precision magnetic field measurements

Sedgwick, J.; Michalson, W.R.; Ludwig, R.;
Instrumentation and Measurement, IEEE Transactions on , Volume: 47 , Issue: 4 , Aug. 1998

Pages:972 - 977

[Abstract] [PDF Full-Text (104 KB)] IEEE JNL

6 Studies of the angular correlation function of scattering by random rough surfaces with and without a buried object

Zhang, G.; Tsang, L.; Kuga, Y.;

Geoscience and Remote Sensing, IEEE Transactions on , Volume: 35 , Issue:

2 , March 1997

Pages:444 - 453

[Abstract] [PDF Full-Text (392 KB)] IEEE JNL

7 Nonlinear clutter cancellation and detection using a memory-based predictor

Leung, H.;

Aerospace and Electronic Systems, IEEE Transactions on , Volume: 32 , Issue:

4 , Oct. 1996

Pages:1249 - 1256

[Abstract] [PDF Full-Text (804 KB)] IEEE JNL

8 LIGHTNING network and systems architecture

Dowd, P.; Perreault, J.; Chu, J.; Hoffmeister, D.C.; Minnich, R.; Burns, D.; Hady, F.; Chen, Y.-J.; Dagenais, M.; Stone, D.;

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